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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/604,517	07/28/2003	Timothy H. Daubenspeck	BUR920020115US1	1516
23389	7590 04/11/2005		EXAMINER	
SCULLY S	COTT MURPHY & P	DUONG, KHANH B		
400 GARDEN CITY PLAZA SUITE 300 GARDEN CITY, NY 11530				
			ART UNIT	PAPER NUMBER
			2822	
			DATE MAILED: 04/11/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	10/604,517	DAUBENSPECK ET AL.			
Office Action Summary	Examiner	Art Unit			
	Khanh B. Duong	2822			
The MAILING DATE of this communication	on appears on the cover sheet with	the correspondence address			
Period for Reply					
A SHORTENED STATUTORY PERIOD FOR F THE MAILING DATE OF THIS COMMUNICAT  - Extensions of time may be available under the provisions of 37 of after SIX (6) MONTHS from the mailing date of this communicated  - If the period for reply specified above is less than thirty (30) days  - If NO period for reply is specified above, the maximum statutory  - Failure to reply within the set or extended period for reply will, by Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	TION.  CFR 1.136(a). In no event, however, may a reption.  s, a reply within the statutory minimum of thirty repriod will apply and will expire SIX (6) MONT y statute, cause the application to become ABA	oly be timely filed  (30) days will be considered timely.  HS from the mailing date of this communication.  NDONED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on	n 21 March 2005.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims					
4) ☐ Claim(s) 1 and 4-15 is/are pending in the 4a) Of the above claim(s) 8-15 is/are with 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1 and 4-7 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction	ndrawn from consideration.				
Application Papers					
9)☐ The specification is objected to by the Ex	aminer.				
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11) The oath or declaration is objected to by	the Examiner. Note the attached	Office Action or form PTO-152.			
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of:  1. Certified copies of the priority docu 2. Certified copies of the priority docu 3. Copies of the certified copies of the application from the International E * See the attached detailed Office action for	uments have been received. uments have been received in Ap e priority documents have been r Bureau (PCT Rule 17.2(a)).	plication No eceived in this National Stage			
Attachment(s)					
1) Notice of References Cited (PTO-892)	4) Interview Su	mmary (PTO-413)			
<ol> <li>Notice of Draftsperson's Patent Drawing Review (PTO-94</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/97</li> <li>Paper No(s)/Mail Date</li> </ol>		Mail Date ormal Patent Application (PTO-152) 			

#### **DETAILED ACTION**

## Continued Examination Under 37 CFR 1.114

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on February 16, 2005 has been entered.

# Response to Amendment

Accordingly, claims 1 and 7 were amended.

Claims 8-15 remain withdrawn from consideration as being directed to a non-elected invention.

Currently, claims 1 and 4-7 are active in this application.

## Response to Arguments

Applicant's arguments with respect to the <u>amended</u> claims have been considered but are moot in view of the new ground(s) of rejection.

# Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to

which said subject matter pertains. Patentability shall not be negatived by the manner in which

the invention was made.

The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.
- 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

Claims 1 and 4-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over the admitted prior art (FIG. 1; Specification, paragraphs [0014] to [0017]) in view of Cook et al. (U.S. Patent No. 6,022,791).

Re claims 1 and 4-7, the admitted prior art ("APA") discloses in FIG. 1 a crack stop for an integrated circuit (IC) chip having an active circuit area, comprising: the IC chip including a bottom substrate, metal layers (M1 to M4) separated by capping layers 16, a top aluminum layer, and copper metal interconnects 18 which do not form a self-passivating oxide layer, in a low-K dielectric material (BPSG); a moisture barrier/edge seal 12 (metal stack: metal lines 22 and via bars 24) positioned along the outer peripheral edges of the active area 10 of the IC chip, a crack stop formed by at least a metal stack outside of the moisture barrier/edge seal 12 on the outer periphery of the IC chip, for preventing damage to the active area of the IC chip caused by chipping and cracking formed along peripheral edges of the IC chip during a dicing operation performed on the IC chip (see Specification, paragraphs [0014] to [0017]).

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Re further claims 1, 6 and 7, the APA discloses the crack stop being formed by at least a metal stack <u>instead of</u> a trench or void region that extends substantially completely between the bottom substrate and the top aluminum layer of the IC chip.

Cook et al. ("Cook"), submitted by Applicant in IDS, teaches in FIG. 3d the use of a crack stop comprising a plurality of trenches or void regions 46 that extends substantially completely between the bottom (silicon) substrate and the top final metal or bond pad layer of the IC chip for the purpose of preventing "propagation of delamination cracks initiated in dicing channel 40" in any layer of the structure" [see col. 1, lines 59-62].

Since the APA and Cook are both from the same field of endeavor, the purpose disclosed by Cook would have been recognized in the pertinent prior art of the APA.

Thus, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the crack stop of the APA in the manner as suggested by Cook because of the desirability to stop propagation of cracks in any layer of the IC chip.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh Duong whose telephone number is (571) 272-1836. The examiner can normally be reached on Monday - Thursday (9:00 AM - 6:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on (571) 272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KBD

AMIR ZAMABIAN

IPERVISORY PATENT EVAMINER

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